

A Joint Coding Method Based on Rateless Coding and Channel Coding at Network Layer

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Abstract—Decoding overhead is a standard for measuring whether rateless coding is good or not. The product-code coding of Shifted RSD, a communication transmission scheme, is based on the joint decoding of rateless coding and channel coding at the network layer of feedback information, was proposed in this study in order to reduce the overhead of decoding in rateless coding in the wireless communication. This method combines rateless coding and channel coding at the network layer to decode, which results in multiple packets instead of one recovered by iterative decoding every time during decoding. The decoding is accomplished until all packets are decoded, and the original information of source is recovered. The decoding overhead used for recovering the original information can be reduced due to the increase in the number of packets recovered at a time. Experimental results show that the use of this method combining channel coding with rateless coding to decode requires less decoding overhead than using LT code or RSD code with feedback for decoding, thus improving the decoding capability of the receiving end.

Index Terms—ARQ, LT code, Overhead, Product code, channel coding

I. INTRODUCTION

The information transmission is a packet-based transmission of information in conventional data transmission system. The information symbols in the sender make up the packet having certain check function through coding in order to avoid decoding failure resulted from transmission error generated due to fading channel. For example, CRC (Cyclic Redundancy Check) is added in the sender. If the packet that receiving end receives is found to be wrong after check, then the receiving end will use ARQ (Automatic Repeat reQuest) to make an ARQ for error packet. The sender of packet receives this request and then sends the packet again. This data transfer is ended until each packet is received by the receiving end without error. The disadvantage of using ARQ to correct the error packet is a time delay caused between ARQ sent by the receiving end and the retransmission of packet in the wireless networks, thus affecting information transmission. The fountain code coding is a rateless encoding method which does not require the ARQ. The sender can code k original packets. There will be a chance to recover k original packets at the receiving end where the packets whose number is greater than k can be received. The design of the fountain codes needs to have decoding overhead as small as possible. The sender codes according to the degree distribution

function, and it is their coding thinking. A steady stream of coding packages is transmitted to the receiving end until the receiving end decodes all packets from sender. RSD (Robust Soliton Distribution) designed by Luby^[1] is now recognized as a better degree distribution function. The use of rateless coding method with the feedback information^[2-3] at the network layer was proposed in order to further reduce decoding overhead. The sender first sends packets independent of each other. The receiving end makes a statistics on the received correct packets and feeds statistical data back to the sender. The combination of feedback information and RSD degree distribution function forms a new degree distribution function. A steady flow of packets obtained through the rateless coding of packets at the sender is transmitted to the receiving end. The receiving end combines the previously received packets with the packets of rateless coding to decode until the information at the sender is obtained.

The packets of rateless coding received at the receiving end all need to go through the CRC check. The error packets will be discarded. However, these packets also contain partially correct original information. Joint coding scheme of network and channel decoding is a coding scheme which uses the spatial redundancy of the network layer and the temporal redundancy of channel coding to further decode the code element symbol to recover the error code element symbol as far as possible. The joint techniques of network and channel coding^[4-6] is proposed in the multiple access channel in the literature. Relaying node is used to transmit information containing redundancy. The combination of network coding and channel coding techniques may also obtain a better gain. The iterative decoding algorithm can also be used for decoding to the product code^[7-9] after Turbo Code Concepts and iterative decoding algorithm were proposed by Berrou, et al. Coding and channel coding at the network layer are combined to construct a product code, which can obtain a better coding gain.

A product-code coding method of Shifted RSD based on feedback is proposed in this paper. It is a coding method combining rateless coding with channel coding at the network layer and is used to improve the decoding capability of rateless encoding.

II. JOINT DECODING OF ERROR CORRECTION PACKET AND CHANNEL CODING

The sender needs to send a large number of packets in the wireless transmission process. There occur errors in part of the packets the receiving end receives. It is neces-

sary to send the feedback to the sender in order to correct these error packets. The sender carries on rateless coding in accordance with certain degree distribution function according to the feedback information. These coded packets are called the error correction packets. The error correction packets are sent to the receiving end, and the error correction method of these error-correction packets is as shown in figure 1.

Black dots represent the correct packets, and white points represent error packets. p_1 , p_2 and p_3 are error correction packets. p_1 and p_2 are connected with the error packets and are error correction packets having a certain error correction function, and the logical relationship formulae (1) and (2) are satisfied between them and the packets at the receiving end:

$$p_1 = s_1 + s_2 + s_3 \pmod{2} \quad (1)$$

$$p_2 = s_4 + s_5 + s_6 + s_7 \pmod{2} \quad (2)$$

Error packet can be informed through the CRC check. The packet can be decoded directly in accordance with rateless decoding method if there is just an error packet in the packets connected with the error correction packet. For example, s_2 is an error packet, and the rest s_1 , s_3 of the packets are correct in p_1 structure. $s_2 = s_1 + s_3 + p_1$ can be obtained by the formula (1), thereby, the error packet s_2 is decoded. The error packets can not be decoded in accordance with the packet relationships if the number of error packets is greater than 1, such as, error packets are s_4 , s_5 and s_7 , respectively in the formula (2). The decoding requires a combination with channel coding.

The packets connected with the error correction packet at the receiving end are all correct sometimes. Such as the packets, s_8 , s_9 and s_{10} , connected to error correction packet p_3 are all correct, thereby, p_3 is a useless error correction packet and does not play an error correction function. Therefore, it should be essential to reduce the number of the useless error correction packets if possible during coding.

Packets transmitted by the sender usually use the channel coding with a certain error correction function, such as linear block code. This channel coding is of a linear relationship with the message code word at the sender. Therefore, the new coding packets got by multiple packets after XOR operation also have a linear relationship with the message codeword. The error correction packet is the packet that the rateless coding on multiple packets at the sender obtains. Therefore, error correction packet and the packet connected with the receiving end form a product code [7-9]. The packets connected to p_2 contain the correct packet in figure 1. The error correction packet p_2 must be correct because the wrong error correction packet can not recover the coding matrix. Wrong error correction packet will be discarded after CRC check. Relationship for error packets and the correct data packet s_6 to satisfy between checks can be obtained by the formula (2).

$p_2 + s_6 \pmod{2}$, as the check packet, meets the mathematical expression (3) of parity check.

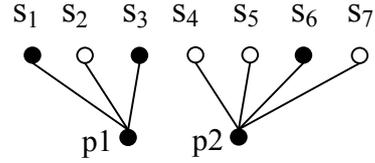


Figure 1. The error correction structure of error correction packets

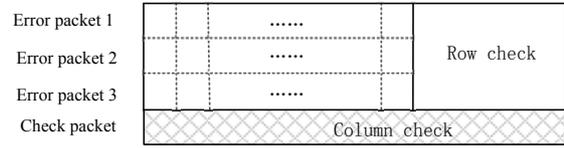


Figure 2. The structure of product code

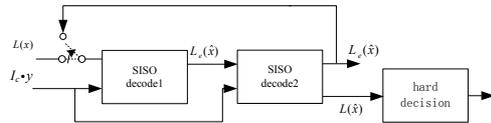


Figure 3. The structure of soft-input, soft-output iterative decoding

$$p_2 + s_6 = s_4 + s_5 + s_7 \quad (3)$$

$p_2 + s_6$ and error packets s_4 , s_5 and s_7 are combined to form a product code with a check packet, and s_4 , s_5 and s_7 can be obtained by using the iterative method. For example, suppose that each packet uses channel coding in formula (3), as shown in figure 2. The check packets and error packets constitute a set of product codes. Both each rows and each columns of product codes satisfy certain constraint relations that can be used to recover some or all packets with errors.

The iterative decoding algorithm of soft input/soft output is used to decode the product code of this structure. The structure of decoding method [10] is as shown in figure 3.

$L(x)$: input is the priori information of information x ; $L_c \cdot y$: channel information; $L(\hat{x})$: output is the soft output of all information bits; $L_e(\hat{x})$: the soft output information of other coded bits. The soft output $L(\hat{x})$ of information bits is the posteriori probability for each coded bit that is given in the form of log-likelihood ratio, as shown in the following formula:

$$L(\hat{x}) = \log \frac{P(x = +1 | y)}{P(x = -1 | y)} = L_c \cdot y + L(x) \quad (4)$$

L_c is channel reliability. $L_e(\hat{x})$ represents extrinsic information, which means the decoder can exchange with extrinsic information by the way of iterative decoding. The decoder 1 decodes the input packets. The decoder 2 decodes the column symbols of multiple input packets. The reliability of the estimated code-element symbol is gradually improved along with the increase in the number of iterations. Iteration does not stop before the correct code word is decoded and the number of iterations reaches the maximum. Therefore, error packet can be decoded by the structure of this product code.

III. THE DISTRIBUTION OF FEEDBACK SHIFTED RSD

Whether rateless coding is good or bad depends on the design of degree distribution function. Rateless coding needs to select the more appropriate degree distribution function in order to reduce the transmission of packets. RSD distribution degree was given in the literature [1].

The receiving end of feedback shifted RSD first receives n correct packages with 1 degree and then feeds this information back to the sender [2]. The rateless coding is made on packets at the sender according to the feedback information. The degree distribution of rateless coding is feedback shifted RSD. The correct packet with 1 degree has been received at the receiving end, which can be seen as the compensation to packet with 1 degree during decoding. Therefore, the feedback shifted RSD is defined as:

$$r_{RSD}(j) = u_{RSD}(i), \text{ for round } \left(\frac{i}{1 - n/k} \right) = j \quad (4)$$

k in the formula above represents the number of packets to be sent by sender, and n represents the number of correct packages the receiving end receives. $u(i)$ is RSD degree distribution function. Round indicates rounding which results in $r(j)$: the degree distribution function of rateless coding of error correction packets at the sender.

Thereby, $r_{RSD}(j)$ after transfer is obtained, and the following formula is obtained through normalization processing:

$$R_{RSD}(j) = \frac{r_{RSD}(j)}{\sum_j r_{RSD}(j)} \quad (5)$$

$R_{RSD}(j)$ is the degree distribution function of rateless coding of single feedback, and l packets are decoded at a time by the function.

IV. THE DESIGN OF SHIFTED RSD PRODUCT CODE OF SINGLE FEEDBACK

The design method of shifted RSD product code with feedback was proposed in accordance with wireless communication. Multiple error packets can be decoded simultaneously each time by this method, thus reducing a decoding overhead. The decoding method combining coding and channel coding at the network layer is called the rateless product-code coding of feedback shifted RSD.

This method allows the sender to transmit packets at twice. The original packet is sent for the first time, and the error correction packet of rateless coding is sent for the second time for the error correction of original error packet.

1) The first sending of packet at the sender:

The sender sends k mutually independent packets in chronological order, and the receiving end checks the received packets, as shown in figure 4. The location of error packet occurrence can be noted according to the sending temporal order if error occurs in the packet. For example, the received error packet will be stored, and its time position will be recorded at the second moment. The number n of correct packets is counted and fed back to the sender after the receiving end receives k packets.

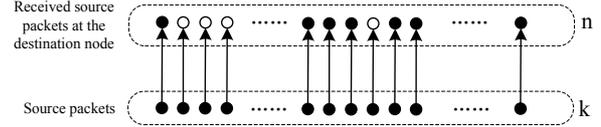


Figure 4. The sending of k independent packets to the receiving end by source

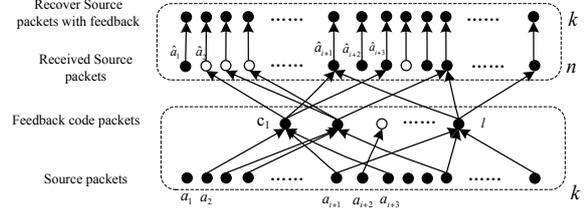


Figure 5. The error corrections of sink error packets by rateless coding packages at the sender

2) The second sending of rateless coding packet at the sender:

The sender makes rateless coding on k packets according to the formula (8) shifted RSD and gets error correction packets. A steady flow of these packets are sent to the receiving end.

Packets are sent in chronological order in the structure in figure5. Therefore, the positions corresponding to sent packets can be identified in accordance with their time sequence although the packets received by the receiving end are wrong and the positions of the packets cannot be decoded. The error packet at receiving end may be decoded according to the location of coding matrix in the error correction packet when the receiving end receives the error correction packet with coding matrix. However, the error correction packet can only be discarded, and the next is waited for if error occurs in the error correction packet, and the carried coding matrix can also not be correctly decoded.

3) Joint error correction decoding:

There is only one error packet connected with the error correction packet sometimes. For example, packets are coded at the sender to obtain $c_1 = a_2 + a_{i+1} + a_{i+3}$ in figure 5. The packet c_1 the receiving end receives is correct, therefore, $c_1 = \hat{a}_2 + \hat{a}_{i+1} + \hat{a}_{i+3}$. The error packet may be directly encoded if there occurs error in only one packet in the structure c_1 . It is known from analysis in the 2 section that a product code constituted by error correction packet and channel coding can be used for the error correction to correct the error packets at the receiving end if there are 2 error packets or more connected with the error correction packet.

The error correction is ended until all error packets are corrected according to the above method. The receiving end sends an ACK to the sender to stop sending coded packets. The coding process is ended, and the packets sent by the sender next time are waited for.

V. PERFORMANCE ANALYSES

Assume an ideal state: the number of product-code iterative decoding is large enough. The errors can all be corrected when the number of error packets is 2 or 3 in the product codes constituted by the error correction packets.

The sender needs to send k equal to 200 packets. This design scheme was compared with no shifted RSD and LT code.

The decoding overheads required for the rateless coding of degree distribution functions of shifted RSD and no shifted RSD are compared in figure 6, where the abscissa represents the rate of error packets.

The feedback shifted RSD scheme is superior to no shifted RSD as can be seen from figure 6. The decoding overhead used by the sender is less than that required for shifted RSD or no shifted RSD when the joint product code of shifted RSD is adopted for error correction, and the number of error packets connected with error correction packet is 2 or 3, respectively. The more the number of error packets corrected by joint product code is, the stronger the decoding capability of the receiving end is. Joint decoding structure is capable of decoding packets of which number is greater than 1 every time. Therefore, it requires decoding overhead clearly superior to shifted RSD coding which can only recover a packet each time.

However, an error also occurs in the error correction packet in the wireless transmission sometimes. The information the error correction packet with an error carries can not be recovered correctly. Therefore, decoding can not be made. The comparison of the number of actual packets the receiving end uses different methods to receive is shown in figure 7.

The LT code, the rateless coding of feedback no shifted RSD, feedback shifted RSD and the product code of feedback shifted RSD are compared in figure 7. The use of product code method of feedback shifted RSD to recover the original information of the sender requires the minimum packets.

VI. CONCLUSIONS

It can be known that the product code method of shifted RSD with feedback presented in this paper can effectively improve the decoding ability of the receiving end from the analysis of above experimental results.

REFERENCES

- [1] Michael Luby, "Lt codes," in *The 43rd Annual IEEE Symposium on Foundations of Computer Science*, pp. 271–282, 2002.
- [2] Hagedorn Andrew, Agarwal Sachin, Starobinski David, Trachtenberg Ari, "Rateless coding with feedback," *INFOCOM 2009, IEEE*, 2009 pp. 1791 – 1799.
- [3] Sørensen, J.H. ; Popovski, P. ; Ostergaard, J., "Feedback in LT Codes for Prioritized and Non-Prioritized Data," *Vehicular Technology Conference (VTC Fall)*, 2012, pp. 1 – 5.
- [4] Tang S., Cheng J., Sun C. et al, "Turbo network coding for efficient and reliable relay," *In Communication Systems, 2008. ICCS 2008*, Nov 2008, pp.1603-1608,.
- [5] Hausl, C. Dupraz, P., "Joint network-channel coding for the multiple-access relay channel," *In 3rd Annual IEEE Communications Society on Sensor and Ad Hoc Communications and Networks*, vol. 3, pp.817-822, Sept 2006.
- [6] Yu Zhang, Zhaoyang Zhang, "Joint Network-Channel Coding with Rateless Code over Multiple Access Relay System," *IEEE Transactions on Wireless Communications*, vol.12, pp. 320 – 332, Jan 2013. <http://dx.doi.org/10.1109/TWC.2012.120312.120633>
- [7] Hagenauer J., Offer E., Papke L., "Iterative decoding of binary block and convolutional codes," *IEEE Trans. Inf. Theory*, vol. 42, no. 2, pp. 429-445, Mar 1996. <http://dx.doi.org/10.1109/18.485714>
- [8] Pyndiah R, "Iterative decoding of product codes: Block turbo codes," *IEEE Int. Symp. Turbo Codes & Related Topics*, vol. 1/1, Brest, France, pp. 71-79, Sept 1997.

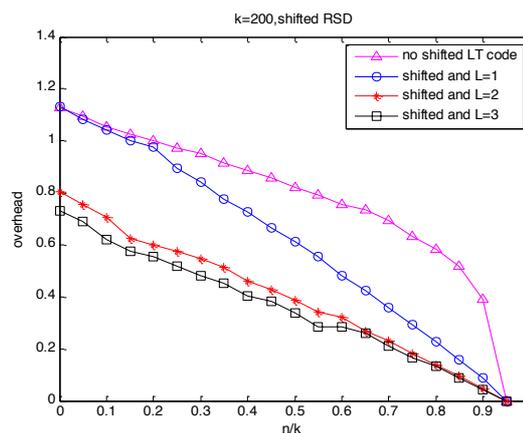


Figure 6. The comparison of decoding overheads required for different ways to recover information at the sender

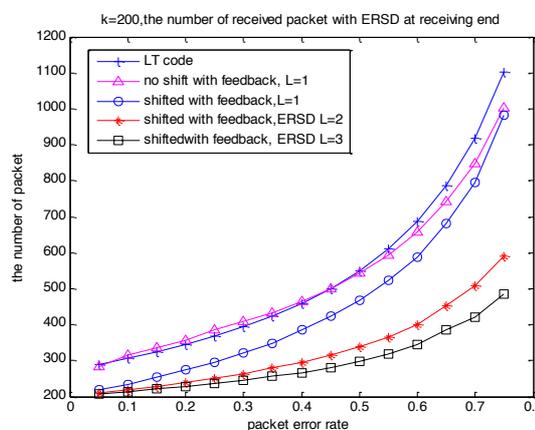


Figure 7. Comparing the number of packets the receiving end needs to receive in the wireless transmission

- [9] Ranking D. M., Gulliver T. A., Taylor D. P, "Parallel and serial concatenated single parity check product codes," *EURASIP Journal on Applied Signal Processing*, no.6, pp. 775-783, May 2005.
- [10] Hagenauer J., Hoehner P, "A Viterbi algorithm with soft-decision outputs and its applications," *IEEE Globecom '89*, 1989, pp.1680-1686.

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