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PAPER

Project-Based Learning as a Tool to Meet Learning Results: A Case Study of Teaching Microcontrollers

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ABSTRACT

This article presents a proposal of project-based learning (PBL) as a didactic tool to meet the learning results (LR) proposed in the syllabus of the course Digital Design with Microcontrollers taught in the Electronic Engineering course of the Faculty of Engineering of the Universidad Distrital Francisco José de Caldas in Bogotá, Colombia. Students are provided with all the information related to the project, the design methodology, what is expected from the project, and how to evaluate the results of the work done. The proposed methodology shows the students that, from a practical project usually applied to the real environment, the theoretical information shown in the classroom is immediately applicable, increasing their motivation and willingness to work. In addition, another skill, teamwork, is reinforced by applying this type of teaching, since each member of the working group (three students per group) has a role in the project's development. The low-cost development kit CY8CKIT-059 for PSoC5LP, manufactured by Infineon Technologies AG, is used in the course to apply the proposal. As a case study to demonstrate the methodology, the design of a data logger that stores humidity and temperature from a digital sensor, developed as one of several projects presented by students in recent semesters, is presented. When comparing the quantitative results obtained from the course in previous semesters with those obtained after applying the project-based learning methodology, a significant improvement can be seen: the percentage of students passing is significantly higher.

KEYWORDS

project-based learning, PSoC5LP, learning results, data logger, I²C

1 INTRODUCTION

The Electronic Engineering program of the Faculty of Engineering of the Universidad Distrital Francisco José de Caldas is a pioneer in this field in Colombia, It began with the founding of the University in 1951 [1,2]. The program was called Radiotechnical Sciences [3]. It is governed by the guidelines of the Ministry of National Education and is currently going through a series of changes that are leading higher

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education along the path of learning results (LR). This regulation is embodied in Decree 1330 of 2019, defining learning results as "the express statements of what a student is expected to know and demonstrate at the time of completing their academic program" [4].

Each of the subject areas that make up the curriculum of the Electronic Engineering program is being (or has been) updated to comply with national regulations; this includes the evaluation and monitoring mechanisms. Thus, the learning results must be supported by a memory (rubric) that is used for subsequent verification [5,6]. The Digital Design with Microcontrollers course is based on designing applications with microcontrollers supported by the laboratories and research groups (e.g., LASER) of the Faculty of Engineering to achieve its learning-results objectives.

1.1 The course Digital Design with Microcontrollers

The Digital Design with Microcontrollers students of the Electronic Engineering degree Faculty of Engineering of the Universidad Distrital Francisco José de Caldas is a part of the curriculum. The course's objectives are "To train professionals capable of identifying, diagnosing, prioritizing and providing solutions to electronic engineering problems that result in the improvement of the quality of life of their community, region and country" [7]. The course must be taken by students in semester VI [8], requiring as prior knowledge a basic level of programming and handling of digital electronics.

1.2 Classification of the courses at the Universidad Distrital

According to Agreement No. 9 of September 12, 2006 [9], Article 10 entitled Classification of Academic Areas defines these as: "The academic areas are classified as Compulsory and Elective. The Compulsory [courses] are classified as Basic and Complementary. The Elective [courses] are classified as Intrinsic to the Programme and Extrinsic to the Programme." [9]. Article 12 of the same agreement, entitled Percentages of Undergraduate Academic Credits, states that: "In undergraduate academic programs, the academic credits corresponding to compulsory [courses] make up between eighty percent (80%) and eighty-five percent (85%) of the total number of credits in the study plan. Likewise, the academic credits corresponding to elective [courses] make up between fifteen percent (15%) and twenty percent (20%) of the total credits of the curriculum... ." [9,10].

1.3 Description, objectives and learning results of the course

The Digital Design with Microcontrollers course is considered, according to the classification presented [9], as a Compulsory,—Basic-type course. According to the information provided in the syllabus of the Digital Design with Microcontrollers course, the main objective of the course is: "To train the student in the analysis, design and construction of applications based on microcontrollers" [11], and the specific objectives are: "To present the student with a synthesis of the main families of microcontrollers on the market, ... introduce the student to the design of digital

systems using microcontrollers, ... introduce the student to various peripherals used to solve problems with microcontrollers (LCD, sensors, etc.), ... [and] show the student high-level software tools used in microcontroller programming" [11].

Regarding the learning results at the end of the course, the student must, among other accomplishments: "Design digital solutions using microcontrollers, ... apply high-level programming solutions to solve problems with microcontrollers, ... [and] use sensors as typical elements in problems associated with microcontroller tasks" [11].

Table 1 shows the distribution of the time that the student must dedicate each week, considering that an academic semester is distributed in 16 weeks, and also shows the time the teacher must also dedicate [11].

Table 1. Dedication in hours of the student and the teacher during the development of the course during the semester [11]

Hours			Hours Professor/Week	Hours Student/Week	Hours Student/Semester	Acad. Credits
DW	CW	AW	(DW + CW)	(DW + CW + AW)	× 16 weeks	
4	2	4	6	10	160	3

Notes: DW, direct work; CW, cooperative work; AW, autonomous work.

The definition of each type of work carried out by each actor involved in the teaching-learning process following Article 16 of Agreement 9/2006 is as follows:

- Hours of direct work (HDW). The HDW are class hours that students must attend in person.
- Hours of cooperative work (HCW). The HCW is class hours that students must attend, according to needs, methodologies and programming defined by the teacher, with the purpose of working individually or in groups, developing topics, advancing practices, solving problems, solving application exercises, trying out applications, etc. Always under the supervision and advice of the teacher.
- Hours of autonomous work (HAW). The HAW are non-teaching hours where the student, without the teacher's presence, autonomously performs tasks and other activities necessary to consolidate their learning in the different academic areas they are studying [9].

This article is organized as follows: Section 2 presents the literature review on the subject of project-based learning (PBL), Section 3 presents the case study corresponding to the design of a data logger that stores the humidity and temperature of a digital sensor using a PSoC5LP [12–15], Section 4 presents the complete development of the proposed problem, showing a system as a solution to it, Section 5 presents the results obtained, and Section 6 shows the conclusions of the methodology presented.

2 LITERATURE REVIEW

On the subject of PBL, multiple studies and varied literature show its application and the results obtained in various teaching areas; some of these studies will be addressed to contextualize the proposal made in this article.

In [16], the authors deal with PBL for a microcontroller programming course making use of an Arduino, where they propose a structural model of the teaching-learning

organization to achieve their goal; they use it for their students to solve three problems, including "Smart Parking," in which evaluation was carried out by a jury of teachers with a list of items to be taken into account. The authors concluded that with the theoretical knowledge acquired in the course, there are tools for problem solving in the real world that allow students to work in groups, assume different roles, and face challenges and solve them. In general, PBL is considered an excellent tool in the teaching-learning process.

The authors of [17], belonging to the Miguel Hernández University of Elche, present a proposal to improve the learning results for a mixed-signal electronics course in the telecommunications engineering field based on PBL. After presenting all the details of the proposal (which is developed individually by the students of the course) and evaluating the proposed project (a line-follower robot), the authors conclude that the grades obtained by the students improved, ostensibly by using the PBL approach versus the traditional evaluation approach. They compared the results obtained with the results of the course form the previous academic period.

In areas such as cybersecurity, it is also possible to apply the PBL approach; in [18], it is shown how PBL contributes to learning in this area. The project proposed to minimize laboratory costs by performing teamwork in such a way that the learning results of the syllabus of the course were met. It was concluded that the PBL approach achieved the learning results for the course above expectations.

PBL applied to industrial computing and robotics teaching is presented in [19], using the LEGO Mindstorms didactic tool, where work was performed autonomously and in groups. After presenting the methodology, the authors conclude that the students learned the course content, that the soft skills that the local industry requires were reinforced, and that the grades were excellent compared with previous years.

In [20], the use of PBL is proposed for teaching what the authors call Virtual Design and Construction (a course taught virtually), hoping to enhance the development of collaborative skills in future professions in Civil Engineering. The results presented show the benefit of the methodology used, improving the students' generic competencies (understanding of theoretical knowledge) by approximately 7%.

The European Union financed the project presented in [21], where PBL was applied to real problems related to public health and sustainable development, focused on high school students, universities, start-up research centers, companies, governmental organizations, and Non-Governmental Organization (NGOs). In conclusion, the project indicates that it was possible to develop competencies in project management and processing scientific evidence, initially generating responsibility, egalitarian participation, and social skills.

In general, it can be concluded from the literature review that PBL is a teaching-learning tool that improves students' skills [22–25], as well as their understanding of the environment of the area in which they are working, which is reflected in the positive results of the evaluation of the project used in the proposal.

3 PROBLEM DEFINITION

A data logger is an electronic device that records data over time or concerning location employing proprietary or externally connected instruments and sensors. Some data loggers communicate with a personal computer and use specific software

to activate the data logger and view and analyze the collected data. In contrast, others have a local interface device (keyboard, LCD) and can be used as a stand-alone device. The vast majority are microcontroller based, usually small, battery powered, portable, and equipped with a microprocessor, internal memory for data storage, and sensors.

One of the main benefits of using data loggers is automatically collecting data 24 hours a day. Upon activation, data loggers usually are left unattended to measure and record information for the duration of the monitoring period. This allows for a comprehensive and accurate overview of the environmental conditions being monitored, such as air temperature and relative humidity [26].

The work requested from the students of the course is the following:

Design a data-logging system (data logger) [27–31] for storing temperature and relative humidity information using an I²C sensor of reference AM2320 in the EEPROM memory of the PSoC5LP; the data read from the sensor must be displayed on a 16×2 alphanumeric LCD and updated once per second.

For the management of the data logger functions, there will be four N.O. buttons with the following functions:

- A push button for Start/Stop data capture, with the start option, temperature and humidity data being captured every five minutes; these are stored in the 2KB EEPROM memory of the PSoC5LP so that up to 512 sets of data can be stored, while in this mode there will be an LED indicating this event.
- The Stop option terminates the process, and the LED is turned off. When the memory is complete, the process is stopped and an additional LED indicating this event is lit.
- Two push buttons to read the data stored in the memory; the data will be displayed on the LCD; one of the push buttons will be used to increment the memory reading and the other one to decrement the memory reading.
- The fourth push button will allow the memory to be cleared when the memory is full.

The learning results, formulated based on the information in the syllabus of the course, which is expected to be obtained after the development of the problem posed, are:

- Apply logic to design solutions with microcontrollers.
- Build digital circuits with microcontrollers.
- Design digital solutions using microcontrollers.
- Apply high-level programming solutions to solve problems with microcontrollers.
- Recognize typical microcontroller peripherals.
- Use sensors as typical elements in the tasks associated with the microcontroller.
- Solve problems in the environment using a microcontroller.

4 DESIGN METHODOLOGY

The methodology used to generate a design that complies with the specifications in Section 3 is presented in Figure 1.



Fig. 1. Methodology used in the design of the problem solution

4.1 Hardware

The PSoC (programmable system-on-chip) is a system that offers innovative integrated capabilities in a single chip, manufactured by the North American company Infineon Technologies AG, with a modern method of acquisition, processing, and control of signals and excellent accuracy, includes an array a Universal Digital Blocks (UDB) and analog that make the PSoC a perfect proposal for the development of engineering projects, providing the system the ability to assign any function to any terminal of the integrated circuit, which confers excellent versatility.

PSoCs employ a configurable architecture for embedded design control, offering the equivalent of an FP-ASIC (field-programmable application-specific integrated circuit) with the advantage of implementation time. PSoC devices integrate configurable digital and analog circuits controlled by an internal microcontroller, thus providing improved design-review capability and requiring fewer components. A single PSoC can integrate up to 100 peripheral functions, saving design time, physical space, and power consumption while improving system quality and reducing cost [32].

Due to the characteristics described above, the PSoC is the right solution for a course focused on the teaching of microcontrollers since it allows linking digital systems (which is the essence of a microcontroller course) with analog components directly and immediately, providing the student with additional tools for general learning in the electronic engineering degree.

Figure 2 shows the detail of the proposed solution to the problem posed, using an I²C block with two bidirectional pins connected to the SCA and SCL lines, an AM2320 reference humidity and temperature sensor, a 2'16 alphanumeric LCD, four N.O. push buttons, and two LEDs.

An I²C Master (fixed-function) block is used with the name I²C, to which no configuration parameters are modified. Associated to the I²C block, two Pins blocks are used in one-bit format, configured as bidirectional pins, with the names SCA and SCL, which have the following configuration: *Number of pins* 1, the *External terminal* option enabled and *Drive mode* with the *Open drain, drives low* option selected. For the correct operation of the I²C bus, a pull-up resistor of 10 k Ω is required.

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Fig. 2. Circuit diagram with a proposed solution

The LCD character block uses the following configuration: *LCD Custom Character Set* none, *Include ASCII to Number Conversion Routines* selected. For the EEPROM block named EEPROM, no configuration is required (the block does not allow any configuration); the whole process of use is done by software; in this case, the entire memory is initialized with the value 0×00 .

Figure 3 shows the breadboard assembly of the proposed solution to the problem with all the hardware components used.



Fig. 3. Assembly of the solved problem on a protoboard

The Timer block, named Timer, has the following configuration: the *Resolution* parameter with the value 16-Bit selected, *Implementation* with the *Fixed Function* option selected, *Period* 30000, which gives a time of five minutes, with the frequency of the *timer_clock* block set to 100 Hz. The *Capture Mode* parameter with *None* selected,

Enable Mode with *Software Only* selected, *Run Mode* in *Continuous* mode, and hardware interrupts disabled. Figure 4 shows the configuration described above.

Configure 'Timer'					
Name:	Timer				
<u></u>	Configure Built	Hin		4 ۵	
	Resolution:	◯ 8-Bit			
	Implementation:	Fixed Function O UDB			
	Period:	300 Max Period = 3s			
	Trigger Mode:	None		~	
	Capture Mode:	None		~	
	Enable Mode:	Software Only		~	
	Run Mode:	Continuous		\sim	
	Interrupts:	On TC On Capture On FIFO Full			
D	latasheet	ОК Арріу	Cance	al III	

Fig. 4. Timer block configuration window

The four N.O. push buttons are all connected to the same four-bit Pins block configured as input with name Function, with the following configuration: *Number of pins* 4, *HW connection* disabled, *External terminal* enabled, and *Drive mode* with the *Resistive pull-up* option selected (this is to ensure a logic "1" on the PSoC input). In the *Input* tab, the *Falling edge* option associated with the *Interrupt* function is selected. Figure 5 shows the configuration described above.

Configure 'Funcion'			? X
Pins Mapping Reset Bu Number of pins: 4 × Ø	ılt-in ↑ ↓ X X		4 Þ
Var pinsj Struncion_0 Struncion_1 Struncion_2 Funcion_3	General Input Type Analog Digital input HW connection Digital output HW connection Output enable Bidirectional External terminal	Dutput Drive mode Resistive pull up	Initial drive state: High (1) ~ Min. supply voltage:
Datasheet		OK Apply	Cancel

Fig. 5. Pin configuration window for push buttons

A second two-bit Pin block is configured as an output, named LED, with the following configuration: *Number of pins 2, HW connection* disabled, *External terminal* option enabled, and *Drive mode* with the *Strong drive* option selected.

The input/output pins (GPIOs) pins used on the PSoC5LP to solve this project are as follows:

- P0.0 pin was used to connect the SCL clock signal of the I2C block.
- P0.1 pin for the SDA data signal of the I2C block.
- P2[7:4] pin array to connect the four N.O. push buttons.
- P2[1:0] pin array for connecting the data logger status LEDs.
- P12[6:0] pin array for the connection of 4 data bus bits and the 3 LCD control bits.

4.2 Software

The algorithm implemented for this project is divided into four processes: the first one is in charge of reading the N. O. push buttons, employing an interruption that allows the selection of the function to be executed in the data logger, the second process is related to the timer that determines the data-saving time in the EEPROM memory of the PSoC5LP.

Data-logger, the second process, is related to the timer that determines the data-saving time in the EEPROM memory of the PSoC5LP; the third process is associated with the sensor data-reading process employing the I²C protocol. The fourth process is performed in the main program, where the peripherals and interrupts are configured, the available EEPROM memory capacity is checked, initial information is displayed, and the temperature and humidity values are displayed on the LCD once per second.

The interrupt routine named, INT_Function(), is in charge of reading the four N. O. push buttons, each performing a different task: one push button for Start/Stop, which is in charge of enabling or disabling the storage of the data read from the sensor in the internal EEPROM memory. Two other push buttons for incremental or decremental data reading allow a review of the information stored in the EEPROM memory. A fourth push button enables the deletion of the entire EEPROM memory.

When the capture function is selected with the corresponding push button, the Timer block is enabled to generate a storage time of five minutes. The INT_Timer() interrupt routine associated with the Timer block reads the sensor data, checks the memory space, and if available, stores the data in the EEPROM memory. If the memory is complete, it disables the Timer and lights an LED to indicate this event; the code generated for this function is shown below.

```
CY ISR(INT Timer) {
                      /Stores data in memory every 5 minutes
  if (vacio==n datos){
                           //If it is full, it reports it
    Timer Stop();
    CyPins SetPin(LED 1);
  }else{
                                 //If not, store the data
    Leer Sensor();
    EEPROM WriteByte(Tabla[4],vacio); //Stores temperature
    EEPROM WriteByte(Tabla[5],vacio+1);
    EEPROM WriteByte(Tabla[2],vacio+2); // Stores humidity
    EEPROM WriteByte(Tabla[3], vacio+3);
    vacio=vacio+4;
  }
  ISR Timer ClearPending();
}
```

The third function, called Read_Sensor(), is in charge of the whole I²C communication process with the sensor reading the temperature and humidity data to be stored in the EEPROM or displayed on the LCD, depending on the routine that has executed it, which is presented below.

```
void Leer_Sensor(void) { // I2C sensor reading function
  I2C MasterSendStart(Sensor Address, I2C WRITE XFER MODE);
  I2C MasterSendStop();//Starts communication with the sensor
  I2C MasterSendStart(Sensor Address, I2C WRITE XFER MODE);
  I2C_MasterWriteByte(0x03); //Sends sequence for data read
  I2C MasterWriteByte(0x00); //First command, then
read start
  I2C_MasterWriteByte(0x04); //address and finally
I2C_MasterSendStop(); //the amount of data to read
  CyDelay(2); //Wait more than 1.5ms to receive a response
  I2C MasterSendStart(Sensor Address,I2C READ XFER MODE);
  for (j=0;j<7;j++) {</pre>
                                     //Reads and stores 7 data
    Tabla[j]=I2C MasterReadByte(I2C ACK DATA);
  }
  Tabla[j]=I2C MasterReadByte(I2C NAK DATA); //Read data 8
  I2C MasterSendStop(); //Sends stop bit to sensor
}
```

Finally, in the main program, the peripherals are configured, the interrupts are enabled, some variables are initialized, the EEPROM memory is checked for accessible memory locations to store data, and a message is displayed with the temperature and humidity data. Finally, in an indefinite repetitive process, the information generated by the sensor is read and displayed once per second.

4.3 Evaluation of the project

After the students have completed the prototype tests, verifying compliance with the requested characteristics, the prototype is presented and evaluated [33–38].

In this case, the following criteria were taken into account for the evaluation of the prototype, for a cumulative rating of up to 5 points:

- Aesthetic and appropriate presentation of the project (0.5 points)
- Compliance with the requirements of the problem to be solved by testing the prototype (2.5 points)
- Adequate use of resources (0.5 points)
- Clarity in the presentation of the project (0.5 points)
- Written report (1 point)

Figure 6 shows the results obtained by each student in the course. It should be noted that the numerical grading scale used at the Universidad Distrital Francisco José de Caldas is in the range of 0.0 to 5.0; the minimum grade to consider an evaluation approved is 3.0. For this specific case, it can be seen that 100% of the students passed the project evaluation.





Figure 7 shows the results grouped by rating ranges (frequency), with a higher concentration in the 3.9 to 4.1 grade range. The mean obtained in the group of 45 students was approximately 4.2.



Fig. 7. Ratings histogram

Comparing the results obtained using the PBL methodology with those of previous semesters, a significant improvement in the overall quantitative results of the course can be observed. Table 2 shows the average for the academic semesters from 2020 to 2022.

Year/Semester	No. Students	Average	No. of Students Approved
2020-1	50	3.7	5
2020-3	48	3.8	6
2021-1	45	3.8	4
2021-3	55	3.5	10
2022-1	42	3.4	8
2022-3	46	3.6	7

5 **RESULTS**

It was evidenced that group work for problem solving based on projects significantly improves the teaching-learning process since it allows students to share information, overcome doubts without fear of making mistakes, and strengthen previous knowledge, among other things.

A problem not taken into account by the professor when setting up the project is related to the socio-economic level of the students; in many cases, the solution initially proposed by them had to be modified because the costs of the prototype exceeded their economic possibilities.

The learning results set out in the Syllabus of the Digital Design with Microcontrollers course were fully met. It should be clarified that it is not possible with a single project to claim to meet all the learning results set out in the syllabus; namely, Design digital solutions using microcontrollers, Apply high-level programming solutions to solve problems with microcontrollers, and Use sensors as typical elements in difficulties associated with microcontroller tasks.

In the technical subject, the students faced a real problem and came up with exciting and technologically feasible solutions, thus demonstrating that project-based learning allows for covering multiple fields in the teaching-learning process of microcontrollers, fully complying with the proposed objectives.

6 CONCLUSIONS

Project-based learning is a pedagogical tool that allows for evaluating many aspects of student performance in a particular academic area, which is of great help in achieving the learning results set out in the syllabus.

According to the evidence and the evaluation rubric, it can be affirmed that the group of students achieved the objectives set for the proposed problem, the design of a data logger, evidencing the fulfillment of the expected learning results. The quantitative evaluation resulted in an average value of 4.6, evidencing a superior development within the evaluation scale used at the university.

When comparing the articles in the literature review with the results obtained in this study, there are many coincidences, such as project-based learning being a pedagogical tool that improves the performance of students as it allows them to deepen in various topics practically, thus strengthening theoretical knowledge, as well as helping them to strengthen other general areas such as group work, autonomous learning, and project management, among others.

7 **REFERENCES**

- [1] C. A. Reina Rodríguez, "Historia de la Universidad Distrital: capítulo: la fundación (1948–1950)," *Revista Científica*, vol. 17, no. 1, pp. 70–85, 2013. <u>https://doi.org/10.14483/</u> 23448350.4566
- [2] Y. C. Carmona, "'Relatos, testimonios y protagonistas', cuenta la historia de la Facultad de Ingeniería," 2022. [Online]. Available: <u>https://agencia.udistrital.edu.co/noticia/relatos-</u>testimonios-y-protagonistas-cuenta-la-historia-de-la-facultad-de-ingenieria
- [3] Universidad Distrital Francisco José de Caldas Tecnología en Electricidad de Media y Baja Tensión, "Reseña Histórica". [Online]. Available: <u>http://tecelectrica.udistrital.edu.</u> co:8080/resena-historica

- [4] Ministerio de Educación Nacional, Decreto 1330 de julio 25 de 2019. Bogotá, Colombia, 2019. [Online]. Available: <u>https://www.mineducacion.gov.co/1780/articles-387348_archivo_</u>pdf.pdf
- [5] F. Gatica-Lara and T. del N. J. Uribarren-Berrueta, "¿Cómo elaborar una rúbrica?" Investigación en Educación Médica, vol. 2, no. 5, pp. 61–65, 2013. <u>https://doi.org/10.1016/</u> S2007-5057(13)72684-X
- [6] D. Shipman, M. Roa, J. Hooten, and Z. J. Wang, "Using the analytic rubric as an evaluation tool in nursing education: The positive and the negative," *Nurse Education Today*, vol. 32, no. 3, pp. 246–249, 2012. https://doi.org/10.1016/j.nedt.2011.04.007
- [7] Universidad Distrital Francisco José de Caldas Ingeniería Electrónica, "Objetivos del Proyecto". [Online]. Available: <u>http://ingelectronica.udistrital.edu.co:8080/mision-</u>vision-objetivos
- [8] Universidad Distrital Francisco José de Caldas Ingeniería Electrónica, "Plan de estudios".
 [Online]. Available: <u>http://ingelectronica.udistrital.edu.co:8080/documents/4239044/</u> 4282970/Pensum_ing_electronica+2015-3.pdf
- [9] Universidad Distrital Francisco José de Caldas Secretaria General, Acuerdo No. 9 de septiembre 12 de 2006, "Por el cual se implementa el Sistema de Créditos Académicos en la Universidad Distrital Francisco José de Caldas". [Online]. Available: <u>https://sgral.</u> udistrital.edu.co/xdata/ca/acu_2006-009.pdf
- [10] M. C. Jiménez, Universidad Distrital Francisco José de Caldas Vicerrectoría Académica, "Lineamientos básicos para la reorganización curricular en el marco de la flexibilidad," 2009. [Online]. Available: <u>https://comunidad.udistrital.edu.co/cic/files/lineamien-</u> tos-b%C3%A1sicos1.pdf
- [11] Universidad Distrital Francisco José de Caldas Ingeniería Electrónica, "Contenidos Programáticos – Pensum 2009-III – Área de Ciencias Básicas de Ingeniería – Digitales - SYLLABUS DISENO DIGITAL CON MICROCONTROLADORES". [Online]. Available: <u>http://ingelectronica.udistrital.edu.co:8080/en/contenidos-programaticos/-/ document_library_display/KfI4/view/7436831/45648?_110_INSTANCE_KfI4_redirect=http%3A%2F%2Fingelectronica.udistrital.edu.co%3A8080%2Fen%2Fcontenidos-programaticos%2F-%2Fdocument_library_display%2FKfI4%2Fview%2F7436831</u>
- [12] J. R. Camargo, O. D. Flórez, and M. A. Ávila, "Didactic system for level control of a water tank," *Elementary Education Online*, vol. 20, no. 6, pp. 996–1006, 2021.
- [13] M. Felgueiras, A. Fidalgo, G. Alves, G. Motta, L. Schlichting, and G. Ferreira, "A remote lab to support e-leaning on Programmable System-on-Chip (PSoC)," 2015 3rd Experiment International Conference (exp.at'15), Ponta Delgada, Portugal, 2015, pp. 167–168. <u>https://</u>doi.org/10.1109/EXPAT.2015.7463257
- [14] D. R. Loker and S. A. Strom, "Programmable system-on-chip (PSoC) usage in embedded programming courses," ASEE (American Society for Engineering Education) Annual Conference and Exposition, Conference Proceedings. 2020, vol. 2020-June.
- [15] J. R. Camargo, C. A. Perdomo, and O. D. Flórez, "Low cost weather station for the colombian geography implemented with PSoC5LP," *ARPN Journal of Engineering and Applied Sciences*, vol. 15, no. 19, pp. 1–6, 2020
- [16] T. T. Zhanat, A. Sagimbayeva, and Z. Kazhiakparova, "Project-based learning technology: An example in programming microcontrollers," *International Journal of Emerging Technologies in Learning (iJET)*, vol. 15, no. 11, pp. 218–227, 2020. <u>https://doi.org/10.3991/jjet.v15i11.13267</u>
- [17] J. M. Blanes, A. Garrigós, D. Marroquí, C. Torres, C. Orts, and P. Casado, "Mixed Signal Electronics Project Based Learning Approach for Engineering Students," 2022 Congreso de Tecnología, Aprendizaje y Enseñanza de la Electrónica (XV Technologies Applied to Electronics Teaching Conference), Teruel, Spain, 2022, pp. 1–6. <u>https://doi.org/10.1109/</u> TAEE54169.2022.9840543

- [18] P. J. Frontera and E. J. Rodríguez-Seda, "Network attacks on cyber-physical systems project-based learning activity," *IEEE Transactions on Education*, vol. 64, no. 2, pp. 110–116, 2021. https://doi.org/10.1109/TE.2020.3014268
- [19] I. Calvo, I. Cabanes, J. Quesada, and O. Barambones, "A multidisciplinary PBL approach for teaching industrial informatics and robotics in engineering," *IEEE Transactions on Education*, vol. 61, no. 1, pp. 21–28, 2018. https://doi.org/10.1109/TE.2017.2721907
- [20] A. Almeida Del Savio, L. D. Zuloeta Carrasco, E. Canahualpa Nakamatsu, K. P. Galantini Velarde, W. Martinez-Alonso, and M. Fischer, "Applying project-based learning (PBL) for teaching virtual design construction (VDC)," *International Journal of Engineering Pedagogy (iJEP)*, vol. 13, no. 2, pp. 64–85, 2023. https://doi.org/10.3991/ijep.v13i2.35877
- [21] C. Santos, E. Rybska, M. Klichowski, B. Jankowiak, S. Jaskulska, N. Domingues, D. Carvalho, T. Rocha, H. Paredes, P. Martins, and J. Rocha, "Science education through project-based learning: A case study," *Procedia Computer Science*, vol. 219, pp. 1713–1720, 2023. https://doi.org/10.1016/j.procs.2023.01.465
- [22] M. Anwar, H. Hidayat, I. P. Yulistiowarno, K. Budayawan, Zulwisli, O. A. Osumah, and Z. Ardi, "Blended learning based project in electronics engineering education courses: A learning innovation after the Covid-19 pandemic," *International Journal of Interactive Mobile Technologies (iJIM)*, vol. 16, no. 14, pp. 107–122, 2022. <u>https://doi.org/10.3991/ijim.</u> v16i14.33307
- [23] T. Gomez-del Rio and J. Rodriguez, "Design and assessment of a project-based learning in a laboratory for integrating knowledge and improving engineering design skills," *Education for Chemical Engineers*, vol. 40, pp. 17–28, 2022. <u>https://doi.org/10.1016/j.</u> ece.2022.04.002
- [24] K. Yelamarthi and E. Drake, "A flipped first-year digital circuits course for engineering and technology students," *IEEE Transactions on Education*, vol. 58, no. 3, pp. 179–186, 2015. https://doi.org/10.1109/TE.2014.2356174
- [25] R. S. Untari, W. Kamdi, A. Dardiri, S. Hadi, and D. Nurhadi, "The development and application of interactive multimedia in project-based learning to enhance students' achievement for 2D animation making," *International Journal of Emerging Technologies in Learning (iJET)*, vol. 15, no. 16, pp. 17–30, 2020. <u>https://doi.org/10.3991/ijet.v15i16.16521</u>
- [26] J. R. Camargo, C. A. Perdomo, and B. Bermúdez, *Proyectos prácticos con PSoC5LP*, 1st Ed., Bogotá: Ediciones de la U, 2019, ISBN: 978-958-792-073-4.
- [27] P. Raichl and P. Marcon, "An IoT-Based Datalogger of Environmental Values," 2021 13th International Conference on Measurement, Bratislava, Slovakia, 2021, pp. 199–202. https://doi.org/10.23919/Measurement52780.2021.9446796
- [28] N. V. Nehovski, N. N. Tomchev, T. S. Djamiykov, and K. K. Asparuhova, "Datalogger for Small Solar Systems," 2018 IX National Conference with International Participation (ELECTRONICA), Sofia, Bulgaria, 2018, pp. 1–3. <u>https://doi.org/10.1109/ELECTRONICA.</u> 2018.8439590
- [29] R. I. Rajkumar, T. J. Alexander, and M. Subramoniam, "Arduino Based Wireless Datalogger for Nuclear Power Plant Using Processing Software," 2017 IEEE International Conference on Computational Intelligence and Computing Research (ICCIC), Coimbatore, India, 2017, pp. 1–4. https://doi.org/10.1109/ICCIC.2017.8524148
- [30] A. López-Vargas, M. Fuentes, M. V. García, and F. J. Muñoz-Rodríguez, "Low-cost datalogger intended for remote monitoring of solar photovoltaic standalone systems based on Arduino[™]," *IEEE Sensors Journal*, vol. 19, no. 11, pp. 4308–4320, 2019. https://doi.org/10.1109/JSEN.2019.2898667
- [31] M. F. Hunar et al., "GSM wireless datalogger of small hydro power generation system," 2014 4th International Conference on Engineering Technology and Technopreneuship (ICE2T), Kuala Lumpur, Malaysia, 2014, pp. 246–251. <u>https://doi.org/10.1109/ICE2T.2014</u>. 7006256

- [32] J. R. Camargo and C. A. Perdomo, *INTRODUCCIÓN AL PSoC5LP: Teoría y aplicaciones prácticas*, 1st Ed., Bogotá: Editorial UD, 2016, ISBN: 978-958-8972-18-3.
- [33] C. Tao, "An evaluation model of the online learning effect based on fuzzy mathematics," *International Journal of Emerging Technologies in Learning (iJET)*, vol. 16, no. 10, pp. 54–70, 2021. https://doi.org/10.3991/ijet.v16i10.22749
- [34] A. Narkglom, E. Boonyapalanant, and P. Koseeyaporn, "Design of Training Media for Internet of Things Training Based on Project-based Learning: A Case Study of Smart Factory Industry," 2019 International Conference on Power, Energy and Innovations (ICPEI), Pattaya, Thailand, 2019, pp. 118–121. <u>https://doi.org/10.1109/ICPEI47862.2019.8944994</u>
- [35] D. Metafas and A. Politi, "Mobile-assisted learning: Designing class project assistant, a research-based educational app for project based learning," 2017 IEEE Global Engineering Education Conference (EDUCON), Athens, 2017, pp. 667–675. <u>https://doi.org/10.1109/</u> EDUCON.2017.7942918
- [36] S. Reis, F. Coelho, and L. Coelho, "Success factors in students' motivation with project based learning: From theory to reality," *International Journal of Online and Biomedical Engineering (iJOE)*, vol. 16, no. 12, pp. 4–17, 2020. https://doi.org/10.3991/ijoe.v16i12.16001
- [37] M. Inoue, Y. Suhara, M. Ichikawa, X. Chen, and T. Wagatsuma, "Planning and Implementation of Large-Scale Online Project-Based Learning and Flipped Classes," 2020 IEEE International Conference on Teaching, Assessment, and Learning for Engineering (TALE), Takamatsu, Japan, 2020, pp. 918–921. https://doi.org/10.1109/TALE48869.2020.9368494
- [38] S. McLoone, B. Lawlor, and A. Meehan, "On Project Oriented Problem Based Learning (POPBL) for a first year engineering circuits project," 25th IET Irish Signals & Systems Conference 2014 and 2014 China-Ireland International Conference on Information and Communications Technologies (ISSC 2014/CIICT 2014), Limerick, 2014, pp. 386–391. https://doi.org/10.1049/cp.2014.0719

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